



NOTICE OF APPEAL FROM THE EXAMINER  
TO THE BOARD OF PATENT APPEALS AND  
INTERFERENCES

AF  
JFW

In re application of: Bailey, et al. Docket No.: ROC920030218US1  
Serial No.: 10/616,683 Group Art Unit: 2112  
Filing Date: 7/10/03 Examiner: Stiglic, Ryan M.

For: COMPUTER APPARATUS AND METHOD FOR AUTONOMICALLY  
DETECTING SYSTEM RECONFIGURATION AND MAINTAINING PERSISTENT  
I/O BUS NUMBERING

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

Applicant or patent owner hereby appeals to the Board of Patent Appeals and Interferences from the decision dated 11/10/05, of the Examiner finally rejecting claims 1-16, 19, 20.


The items checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection.  
a. ☐ was obtained on \_\_\_\_\_ for \_\_\_\_\_ months(s).  
b. ☐ is hereby requested under 37 CFR 1.136.
2. ☒ Appeal fee amount \$500.00  
☐ Extension of time fee amount \$0.00  
a. ☐ Enclosed  
b. ☒ Charge to Deposit Account No. 09-0465 for IBM Corporation

One additional copy of this Notice is enclosed herewith.

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Respectfully submitted,

  
Bret J. Petersen  
Reg. No. 37,417

02/14/2006 CCHAU1 00000005 10616683  
01 FC:1401 500.00 DA

I HEREBY CERTIFY THAT THE CORRESPONDENCE TO WHICH THIS STATEMENT IS AFFIXED IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE, POSTAGE PAID, AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO: MAIL STOP AF, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450, ON: 2-10-06

SIGNED: 

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